

**IN THE SPECIFICATION**

Please replace the paragraph on Page 1, beginning on Line 1 as follows:

**RELATED APPLICATIONS**

This application is related to co-pending U.S. Patent Application Serial No. 10/783,070, entitled "Adaptive Equalizer with DC Offset Compensation," filed February 20, 2004 (Attorney Docket No. 073338.0173).

On Page 6, beginning on line 1, please replace the paragraph as follows:

**DETAILED DESCRIPTION OF THE INVENTION**

FIGURE 1 illustrates an equalizer 100 that compensates for attenuation in a signal communicated to equalizer 100 using a communication medium. In the depicted embodiment, equalizer 100 includes an adaptive controller 102 that adjusts the amount of gain applied to each of three signal paths 101A, 101B, and 101C based on an output characteristic of the output signal measured by an output monitor 104. Equalizer 100 also includes an offset controller 106 that corrects for DC offsets imparted by components of equalizer 100 in response to offset monitor 108 detecting an improper DC offset in the output signal of equalizer 100. Other components of equalizer 100 include variable gain limiting amplifier 110, mathematical operators 112, delay generators 114, variable gain amplifiers 116, mixer 118, and drive amplifier 120. In general, equalizer 100 provides an equalized output signal with a corrected DC offset despite distortions in the input signal due to attenuation in communication media and variations in the DC offset of the signal potentially caused by fabrication technology, such as device geometry mismatch or threshold voltage mismatch.